Dirk Beyer

Joint work with Tom Henzinger, Erkan Keremoglu, Philipp Wendler



Software Verification

C program



Safety Properties

General method: Create an overapproximation of the program states



Model Checking

Program + Property



ACM Turing Award 2007

Edmund Clarke Allen Emmerson Joseph Sifakis

Invention: "Model Checking"

Classic Model Checking



Classic Model Checking



Classic Model Checking



Enormous amounts of resources wasted!





SAFE under Condition Φ

- Examples: $-\Phi =$ true: previous SAFE $-\Phi =$ false: previous UNSAFE
 - general: condition for safety



- Never crash!
 - \rightarrow I Condition: specify time and memory
- Always dump results!
 → O Condition: report partial results
- Sequential composition \rightarrow Solve harder problems
- Comparison of Checkers:
 - → Winner is who has Weakest Condition! (i.e., has proved "most")

Input Conditions

Two main use cases for input conditions

- A) Use conditions for restricting the search (similar to bounded model checking)
- B) Use output of another model checker

Quantitative Input Conditions

Component	Name	Condition	Impl.
Global	Total Time	time	\checkmark
Progress	Total Space	mem	\checkmark
e	# Abstract States	reached	\checkmark
	# Abstract States per Loc.	#(reached, loc)
	Busy Edge	#(edge)	Í √
Post	Time for Post	$time(\rightsquigarrow)$	\checkmark
Computation	Space for Post	$mem(\rightsquigarrow)$	
	Size of State	mem(state)	
	Path Length	length(path)	\checkmark
	Time Spent in Path	time(path)	\checkmark
	Repeating Locs. in Path	#(path, loc)	\checkmark
	Assume Edges in Path	assumes(path)) 🗸
CEX	Time for Refinement	time(ref)	\checkmark
Analysis	Space for Refinement	mem(ref)	
2	Size of Path Formula	mem(pf)	\checkmark

Input Condition: Path Length



Input Condition: Path Length

1	<pre>void main() {</pre>
2	int x = 1;
3	if (nondet_int()) {
4	while (x < 10000) {
5	x++;
6	}
7	assert (x == 10000);
8	} else {
9	x = 0;
10	}
11	assert (x != 0);
12	}



		C	BMC				CPACHE	CKER		
					Expli	cit	Explic Explic	it	Predica	ate
	k =	1	k = 10)	time(1	0s				
cdaudio_simpl1	1.0	~	1.0	~	3.5	~	3.2	~	17	~
cdaudio_simpl1_BUG	.99	\checkmark	.99	\checkmark	2.8	\checkmark	2.5	\checkmark	10	\checkmark
diskperf_simpl1	.25	-	.26	-	13	-	900	-	15	\checkmark
floppy_simpl3	.16	\checkmark	.15	\checkmark	2.9	\checkmark	2.4	\checkmark	8.5	\checkmark
floppy_simpl3_BUG	.16	\checkmark	.16	\checkmark	2.2	\checkmark	2.5	\checkmark	7.8	\checkmark
floppy_simpl4	.28	\checkmark	.28	\checkmark	3.5	\checkmark	3.7	\checkmark	12	\checkmark
floppy_simpl4_BUG	.30	\checkmark	.30	\checkmark	3.0	\checkmark	2.9	\checkmark	11	\checkmark
kbfiltr_simpl1	.05	\checkmark	.06	\checkmark	3.1	\checkmark	2.2	\checkmark	3.7	\checkmark
kbfiltr_simpl2	.11	\checkmark	.10	\checkmark	3.1	\checkmark	3.2	\checkmark	5.2	\checkmark
kbfiltr_simpl2_BUG	.11	\checkmark	.12	\checkmark	2.2	\checkmark	2.1	\checkmark	4.1	\checkmark
NT drivers total	3.4	9	3.4	9	39	9	920	9	94	10
s3_clnt_1	.03	-	4.3	-	9.8	\checkmark	8.3	~	8.1	~
s3_clnt_1_BUG	.03	-	4.2	-	4.4	\checkmark	3.6	\checkmark	6.2	\checkmark
s3_clnt_2	.03	-	4.6	-	10	\checkmark	8.2	\checkmark	7.2	\checkmark
s3_clnt_2_BUG	.03	-	4.3	-	4.5	\checkmark	3.5	\checkmark	5.4	\checkmark
s3_clnt_3	.03	-	5.3	-	9.7	\checkmark	8.1	\checkmark	5.8	\checkmark
s3_clnt_3_BUG	.03	-	5.3	\checkmark	5.1	\checkmark	3.5	\checkmark	6.0	\checkmark
s3_clnt_4	.03	-	4.8	-	9.8	\checkmark	8.5	\checkmark	10	\checkmark
s3_clnt_4_BUG	.03	-	4.3	-	4.3	\checkmark	3.5	\checkmark	6.4	\checkmark
s3_srvr_1	.03	-	4.1	-	3.3	\checkmark	2.4	\checkmark	21	\checkmark
s3_srvr_1_BUG	.03	-	6.4	\checkmark	2.2	\checkmark	1.7	\checkmark	4.8	\checkmark
s3_srvr_2	.03	-	5.5	-	2.8	\checkmark	2.4	\checkmark	150	\checkmark
s3_srvr_2_BUG	.03	-	6.2	\checkmark	1.8	\checkmark	1.7	\checkmark	4.1	\checkmark
s3_srvr_3	.03	-	5.6	-	3.6	-	2.6	-	9.0	\checkmark
s3_srvr_4	.03	-	5.6	-	3.1	-	2.5	-	28	\checkmark
s3_srvr_6	.03	-	6.6	-	14	-	250	\checkmark	230	\checkmark
s3_srvr_7	.03	-	6.3	-	14	-	200	\checkmark	47	\checkmark
s3_srvr_8	.03	-	6.1	-	2.8	\checkmark	3.0	\checkmark	23	\checkmark
SSH total	0.51	0	90	3	110	13	510	15	570	17
bist_cell	.02	-	.65	-	9.4	-	9.2	-	210	\checkmark
kundu	.02	-	120	-	7.3	\checkmark	6.2	\checkmark	900	-
kundu 1_BUG	.02	-	18	-	2.0	\checkmark	1.8	\checkmark	15	\checkmark
kundu2_BUG	.02	-	120	-	1.6	\checkmark	1.9	\checkmark	510	\checkmark
pc_sfifo_1	.02	-	12	-	14	-	900	-	5.3	\checkmark
pc_sfifo_2	.02	-	6.3	-	14	-	900	-	9.8	\checkmark
token_ring.01	.02	-	16	-	1.7	-	1.6	-	8.1	\checkmark
toy2_BUG	.03	-	30	-	2.7	\checkmark	2.1	\checkmark	65	\checkmark

		C	BMC				CPACHE	CKER			Comb.	A	Comb	ь. B
					Explie	cit	Explic	it	Predica	ate	Expl. + P	red.	CBMC+Ex	pl.+Pred.
	k =	1	k = 10)	time(1	(0s)					-			
cdaudio_simpl1	1.0	\checkmark	1.0	\checkmark	3.5	\checkmark	3.2	\checkmark	17	√	3.5	\checkmark	1.0	√
cdaudio_simpl1_BUG	.99	\checkmark	.99	\checkmark	2.8	\checkmark	2.5	\checkmark	10	\checkmark	2.8	\checkmark	.99	\checkmark
diskperf_simpl1	.25	-	.26	-	13	-	900	-	15	\checkmark	28	\checkmark	28	\checkmark
floppy_simpl3	.16	\checkmark	.15	\checkmark	2.9	\checkmark	2.4	\checkmark	8.5	\checkmark	2.9	\checkmark	.16	\checkmark
floppy_simpl3_BUG	.16	\checkmark	.16	\checkmark	2.2	\checkmark	2.5	\checkmark	7.8	\checkmark	2.2	\checkmark	.16	\checkmark
floppy_simpl4	.28	\checkmark	.28	\checkmark	3.5	\checkmark	3.7	\checkmark	12	\checkmark	3.5	\checkmark	.28	\checkmark
floppy_simpl4_BUG	.30	\checkmark	.30	\checkmark	3.0	\checkmark	2.9	\checkmark	11	\checkmark	3.0	\checkmark	.30	\checkmark
kbfiltr_simpl1	.05	\checkmark	.06	\checkmark	3.1	\checkmark	2.2	\checkmark	3.7	\checkmark	3.1	\checkmark	.05	\checkmark
kbfiltr_simpl2	.11	\checkmark	.10	\checkmark	3.1	\checkmark	3.2	\checkmark	5.2	\checkmark	3.1	\checkmark	.11	\checkmark
kbfiltr_simpl2_BUG	.11	\checkmark	.12	\checkmark	2.2	\checkmark	2.1	\checkmark	4.1	\checkmark	2.2	\checkmark	.11	\checkmark
NT drivers total	3.4	9	3.4	9	39	9	920	9	94	10	54	10	31	10
s3_clnt_1	.03	-	4.3	-	9.8	\checkmark	8.3	~	8.1	~	9.8	~	9.8	√
s3_clnt_1_BUG	.03	-	4.2	-	4.4	\checkmark	3.6	\checkmark	6.2	\checkmark	4.4	\checkmark	4.4	\checkmark
s3_clnt_2	.03	-	4.6	-	10	\checkmark	8.2	\checkmark	7.2	\checkmark	10	\checkmark	10	\checkmark
s3_clnt_2_BUG	.03	-	4.3	-	4.5	\checkmark	3.5	\checkmark	5.4	\checkmark	4.5	\checkmark	4.5	\checkmark
s3_clnt_3	.03	-	5.3	-	9.7	\checkmark	8.1	\checkmark	5.8	\checkmark	9.7	\checkmark	9.7	\checkmark
s3_clnt_3_BUG	.03	-	5.3	\checkmark	5.1	\checkmark	3.5	\checkmark	6.0	\checkmark	5.1	\checkmark	5.1	\checkmark
s3_clnt_4	.03	-	4.8	-	9.8	\checkmark	8.5	\checkmark	10	\checkmark	9.8	\checkmark	9.8	\checkmark
s3_clnt_4_BUG	.03	-	4.3	-	4.3	\checkmark	3.5	\checkmark	6.4	\checkmark	4.3	\checkmark	4.3	\checkmark
s3_srvr_1	.03	-	4.1	-	3.3	\checkmark	2.4	\checkmark	21	\checkmark	3.3	\checkmark	3.3	\checkmark
s3_srvr_1_BUG	.03	-	6.4	\checkmark	2.2	\checkmark	1.7	\checkmark	4.8	\checkmark	2.2	\checkmark	2.2	\checkmark
s3_srvr_2	.03	-	5.5	-	2.8	\checkmark	2.4	\checkmark	150	\checkmark	2.8	\checkmark	2.8	\checkmark
s3_srvr_2_BUG	.03	-	6.2	\checkmark	1.8	\checkmark	1.7	\checkmark	4.1	\checkmark	1.8	\checkmark	1.8	\checkmark
s3_srvr_3	.03	-	5.6	-	3.6	-	2.6	-	9.0	\checkmark	13	\checkmark	13	\checkmark
s3_srvr_4	.03	-	5.6	-	3.1	-	2.5	-	28	\checkmark	31	\checkmark	31	\checkmark
s3_srvr_6	.03	-	6.6	-	14	-	250	\checkmark	230	\checkmark	240	\checkmark	240	\checkmark
s3_srvr_7	.03	-	6.3	-	14	-	200	\checkmark	47	\checkmark	61	\checkmark	61	\checkmark
s3_srvr_8	.03	-	6.1	-	2.8	\checkmark	3.0	\checkmark	23	\checkmark	2.8	\checkmark	2.8	\checkmark
SSH total	0.51	0	90	3	110	13	510	15	570	17	420	17	420	17
bist_cell	.02	-	.65	-	9.4	-	9.2	-	210	√	220	√	220	~
kundu	.02	-	120	-	7.3	\checkmark	6.2	\checkmark	900	-	7.3	\checkmark	7.3	\checkmark
kundu1_BUG	.02	-	18	-	2.0	\checkmark	1.8	\checkmark	15	\checkmark	2.0	\checkmark	2.0	\checkmark
kundu2_BUG	.02	-	120	-	1.6	\checkmark	1.9	\checkmark	510	\checkmark	1.6	\checkmark	1.6	\checkmark
pc_sfifo_1	.02	-	12	-	14	-	900	-	5.3	\checkmark	19	\checkmark	19	\checkmark
pc_sfifo_2	.02	-	6.3	-	14	-	900	-	9.8	\checkmark	24	\checkmark	24	\checkmark
token_ring.01	.02	-	16	-	1.7	-	1.6	-	8.1	\checkmark	9.8	\checkmark	9.8	\checkmark
toy2_BUG	.03	-	30	-	2.7	\checkmark	2.1	\checkmark	65	\checkmark	2.7	\checkmark	2.7	\checkmark

kbiiltr_simpl2_BUG	11.	V	.12	√	2.2	√	2.1	√	4.1	√	2.2	√	.11	v
NT drivers total	3.4	9	3.4	9	39	9	920	9	94	10	54	10	31	10
s3_clnt_1	.03	-	4.3	-	9.8	~	8.3	~	8.1	~	9.8	~	9.8	~
s3_clnt_1_BUG	.03	-	4.2	-	4.4	\checkmark	3.6	\checkmark	6.2	\checkmark	4.4	\checkmark	4.4	\checkmark
s3_clnt_2	.03	-	4.6	-	10	\checkmark	8.2	\checkmark	7.2	\checkmark	10	\checkmark	10	\checkmark
s3_clnt_2_BUG	.03	-	4.3	-	4.5	\checkmark	3.5	\checkmark	5.4	\checkmark	4.5	\checkmark	4.5	\checkmark
s3_clnt_3	.03	-	5.3	-	9.7	\checkmark	8.1	\checkmark	5.8	\checkmark	9.7	\checkmark	9.7	\checkmark
s3_clnt_3_BUG	.03	-	5.3	\checkmark	5.1	\checkmark	3.5	\checkmark	6.0	\checkmark	5.1	\checkmark	5.1	\checkmark
s3_clnt_4	.03	-	4.8	-	9.8	\checkmark	8.5	\checkmark	10	\checkmark	9.8	~	9.8	\checkmark
s3_clnt_4_BUG	.03	-	4.3	-	4.3	\checkmark	3.5	\checkmark	6.4	\checkmark	4.3	\checkmark	4.3	\checkmark
s3_srvr_1	.03	-	4.1	-	3.3	\checkmark	2.4	\checkmark	21	\checkmark	3.3	\checkmark	3.3	\checkmark
s3_srvr_1_BUG	.03	-	6.4	\checkmark	2.2	\checkmark	1.7	\checkmark	4.8	\checkmark	2.2	~	2.2	\checkmark
s3_srvr_2	.03	-	5.5	-	2.8	\checkmark	2.4	\checkmark	150	\checkmark	2.8	~	2.8	\checkmark
s3_srvr_2_BUG	.03	-	6.2	\checkmark	1.8	\checkmark	1.7	\checkmark	4.1	\checkmark	1.8	\checkmark	1.8	\checkmark
s3_srvr_3	.03	-	5.6	-	3.6	-	2.6	-	9.0	\checkmark	13	\checkmark	13	\checkmark
s3_srvr_4	.03	-	5.6	-	3.1	-	2.5	-	28	\checkmark	31	\checkmark	31	\checkmark
s3_srvr_6	.03	-	6.6	-	14	-	250	\checkmark	230	\checkmark	240	\checkmark	240	\checkmark
s3_srvr_7	.03	-	6.3	-	14	-	200	\checkmark	47	\checkmark	61	\checkmark	61	\checkmark
s3_srvr_8	.03	-	6.1	-	2.8	\checkmark	3.0	\checkmark	23	\checkmark	2.8	\checkmark	2.8	\checkmark
SSH total	0.51	0	90	3	110	13	510	15	570	17	420	17	420	17
bist_cell	.02	-	.65	-	9.4	-	9.2	-	210	\checkmark	220	~	220	~
bist_cell kundu	.02 .02	-	.65 120	-	9.4 7.3	~	9.2 6.2	~	210 900	√ -	220 7.3	√ ✓	220 7.3	√ √
bist_cell kundu kundu1_BUG	.02 .02 .02	- - -	.65 120 18	- - -	9.4 7.3 2.0	- ~ ~	9.2 6.2 1.8	- ~ ~	210 900 15	✓ - ✓	220 7.3 2.0	~ ~ ~	220 7.3 2.0	\$ \$ \$
bist_cell kundu kundu1_BUG kundu2_BUG	.02 .02 .02 .02	- - -	.65 120 18 120	- - -	9.4 7.3 2.0 1.6	- ~ ~ ~	9.2 6.2 1.8 1.9	- ~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	210 900 15 510	< - < <	220 7.3 2.0 1.6	~ ~ ~ ~	220 7.3 2.0 1.6	\$ \$ \$
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1	.02 .02 .02 .02 .02		.65 120 18 120 12	- - - -	9.4 7.3 2.0 1.6 14	- - - -	9.2 6.2 1.8 1.9 900	-	$210 \\ 900 \\ 15 \\ 510 \\ 5.3$	~ ~ ~ ~ ~	220 7.3 2.0 1.6 19	~ ~ ~ ~ ~	220 7.3 2.0 1.6 19	\$ \$ \$ \$
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2	.02 .02 .02 .02 .02 .02 .02		$ \begin{array}{r} .65 \\ 120 \\ 18 \\ 120 \\ 12 \\ 6.3 \\ \end{array} $	- - - -	$9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14$		9.2 6.2 1.8 1.9 900 900		$210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8$	~ ~ ~ ~ ~ ~	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24$	< < < < < <	220 7.3 2.0 1.6 19 24	~ ~ ~ ~ ~
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01	.02 .02 .02 .02 .02 .02 .02 .02		$ \begin{array}{r} .65 \\ 120 \\ 18 \\ 120 \\ 12 \\ 6.3 \\ 16 \\ 16 $		$9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7$		$9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6$		$210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1$	~ ~ ~ ~ ~ ~ ~	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8$	< < < < < < <	220 7.3 2.0 1.6 19 24 9.8	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03		$ \begin{array}{r} .65 \\ 120 \\ 18 \\ 120 \\ 12 \\ 6.3 \\ 16 \\ 30 \\ \end{array} $		$9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7$		$9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6 \\ 2.1$		$210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1 \\ 65$	< < < < < < <	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7$	< < < < < < < < < < < < < < < < < < <	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7$	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3 \end{array}$		$9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 1.7 \\ $		$9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6 \\ 2.1 \\ 1.5$		$210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1 \\ 65 \\ 2.2$	< < < < < < < < < <	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ $	< < < < < < < < < < < < < < < < < < <	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ $	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.02.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02 .02		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ \end{array}$		$9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ $		9.2 6.2 1.8 1.9 900 900 1.6 2.1 1.5 1.7		$210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1 \\ 65 \\ 2.2 \\ 5.1$	~ ~ ~ ~ ~ ~ ~ ~ ~ ~	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \end{array}$	< < < < < < < < < < < < < < < < < < <	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ $	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02 .02 .03		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ \end{array}$	- - - - - - - - - - - - - - - - - - -	$9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.0 \\ $		$\begin{array}{r} 9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6 \\ 2.1 \\ 1.5 \\ 1.7 \\ 1.8 \end{array}$		$210 \\900 \\15 \\510 \\5.3 \\9.8 \\8.1 \\65 \\2.2 \\5.1 \\36$	< - < < < < < < < < < < < < < < < < < <	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \end{array}$	< < < < < < < < < < < < < < < < < < <	$220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.0 \\ $	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG transmitter.04.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02 .03 .03		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ 270\\ \end{array}$	- - - - - - - - - - - - -	$\begin{array}{r} 9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \end{array}$		$\begin{array}{r} 9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6 \\ 2.1 \\ 1.5 \\ 1.7 \\ 1.8 \\ 2.5 \end{array}$		$\begin{array}{c} 210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1 \\ 65 \\ 2.2 \\ 5.1 \\ 36 \\ 900 \end{array}$		$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \end{array}$	< < < < < < < < < < < < < < < < < < <	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \end{array}$	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG transmitter.04.BUG transmitter.05.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .03 .03 .03 .04		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ 270\\ 670\\ \end{array}$		$\begin{array}{r} 9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \end{array}$		$\begin{array}{r} 9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6 \\ 2.1 \\ 1.5 \\ 1.7 \\ 1.8 \\ 2.5 \\ 2.3 \end{array}$		$\begin{array}{c} 210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1 \\ 65 \\ 2.2 \\ 5.1 \\ 36 \\ 900 \\ 900 \\ 900 \end{array}$	- < < < < < < < < < < < < < < < < < < <	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \end{array}$	< < < < < < < < < < < < < < < < < < <	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \end{array}$	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG transmitter.04.BUG transmitter.05.BUG transmitter.06.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02 .03 .03 .04 .04		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ 270\\ 670\\ 900\\ \end{array}$	- - - - - - - - - - - - - - - - - - -	$\begin{array}{r} 9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \end{array}$	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	$\begin{array}{r} 9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6 \\ 2.1 \\ 1.5 \\ 1.7 \\ 1.8 \\ 2.5 \\ 2.3 \\ 3.0 \end{array}$		$\begin{array}{c} 210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1 \\ 65 \\ 2.2 \\ 5.1 \\ 36 \\ 900 \\ 900 \\ 900 \\ 900 \end{array}$	- < < < < < < < < < < < < < < < < < < <	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \end{array}$	<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \end{array}$	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG transmitter.04.BUG transmitter.05.BUG transmitter.06.BUG transmitter.07.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02 .03 .03 .04 .04 .04		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ 270\\ 670\\ 900\\ 900\\ 900\\ \end{array}$		$\begin{array}{r} 9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \\ 3.8 \end{array}$	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	$\begin{array}{r} 9.2 \\ 6.2 \\ 1.8 \\ 1.9 \\ 900 \\ 900 \\ 1.6 \\ 2.1 \\ 1.5 \\ 1.7 \\ 1.8 \\ 2.5 \\ 2.3 \\ 3.0 \\ 3.3 \end{array}$	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	$\begin{array}{c} 210 \\ 900 \\ 15 \\ 510 \\ 5.3 \\ 9.8 \\ 8.1 \\ 65 \\ 2.2 \\ 5.1 \\ 36 \\ 900 \\ 900 \\ 900 \\ 900 \\ 900 \\ 900 \\ 900 \end{array}$		$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \\ 3.8 \end{array}$	< < < < < < < < < < < < < < < < < < <	$\begin{array}{c} 220 \\ 7.3 \\ 2.0 \\ 1.6 \\ 19 \\ 24 \\ 9.8 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \\ 3.8 \end{array}$	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG transmitter.04.BUG transmitter.05.BUG transmitter.07.BUG transmitter.08.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02 .03 .03 .03 .04 .04 .04 .05		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ 270\\ 670\\ 900\\ 900\\ 900\\ 900\\ 900\\ \end{array}$		$\begin{array}{r} 9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \\ 3.8 \\ 6.3 \end{array}$	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	$\begin{array}{r} 9.2\\ 6.2\\ 1.8\\ 1.9\\ 900\\ 900\\ 1.6\\ 2.1\\ 1.5\\ 1.7\\ 1.8\\ 2.5\\ 2.3\\ 3.0\\ 3.3\\ 4.6\end{array}$	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	$\begin{array}{c} 210\\ 900\\ 15\\ 510\\ 5.3\\ 9.8\\ 8.1\\ 65\\ 2.2\\ 5.1\\ 36\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 90$		$\begin{array}{c} 220\\ 7.3\\ 2.0\\ 1.6\\ 19\\ 24\\ 9.8\\ 2.7\\ 1.7\\ 2.0\\ 2.0\\ 2.2\\ 2.5\\ 3.4\\ 3.8\\ 6.3\\ \end{array}$	<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<	$\begin{array}{c} 220\\ 7.3\\ 2.0\\ 1.6\\ 19\\ 24\\ 9.8\\ 2.7\\ 1.7\\ 2.0\\ 2.0\\ 2.2\\ 2.5\\ 3.4\\ 3.8\\ 6.4 \end{array}$	
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG transmitter.04.BUG transmitter.05.BUG transmitter.07.BUG transmitter.08.BUG transmitter.09.BUG	.02 .02 .02 .02 .02 .02 .02 .03 .02 .03 .03 .03 .04 .04 .04 .05 .05		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ 270\\ 670\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 9$		$\begin{array}{r} 9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \\ 3.8 \\ 6.3 \\ 11 \end{array}$	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	$\begin{array}{r} 9.2\\ 6.2\\ 1.8\\ 1.9\\ 900\\ 900\\ 1.6\\ 2.1\\ 1.5\\ 1.7\\ 1.8\\ 2.5\\ 2.3\\ 3.0\\ 3.3\\ 4.6\\ 9.4 \end{array}$	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	210 900 15 510 5.3 9.8 8.1 65 2.2 5.1 36 900	$\overline{}$	$\begin{array}{c} 220\\ 7.3\\ 2.0\\ 1.6\\ 19\\ 24\\ 9.8\\ 2.7\\ 1.7\\ 2.0\\ 2.0\\ 2.2\\ 2.5\\ 3.4\\ 3.8\\ 6.3\\ 11\\ \end{array}$	<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<	$\begin{array}{c} 220\\ 7.3\\ 2.0\\ 1.6\\ 19\\ 24\\ 9.8\\ 2.7\\ 1.7\\ 2.0\\ 2.0\\ 2.2\\ 2.5\\ 3.4\\ 3.8\\ 6.4\\ 11\\ \end{array}$	~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~
bist_cell kundu kundu1_BUG kundu2_BUG pc_sfifo_1 pc_sfifo_2 token_ring.01 toy2_BUG transmitter.01.BUG transmitter.03.BUG transmitter.04.BUG transmitter.05.BUG transmitter.06.BUG transmitter.07.BUG transmitter.08.BUG transmitter.09.BUG	.02 .02 .02 .02 .02 .02 .02 .02 .03 .02 .03 .03 .03 .04 .04 .04 .04 .05 .05 .05		$\begin{array}{r} .65\\ 120\\ 18\\ 120\\ 12\\ 6.3\\ 16\\ 30\\ 5.3\\ 21\\ 92\\ 270\\ 670\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 5000\\ \end{array}$	- - - - - - - - - - - - - - - - - - -	$\begin{array}{r} 9.4 \\ 7.3 \\ 2.0 \\ 1.6 \\ 14 \\ 14 \\ 1.7 \\ 2.7 \\ 1.7 \\ 2.0 \\ 2.0 \\ 2.2 \\ 2.5 \\ 3.4 \\ 3.8 \\ 6.3 \\ 11 \\ \hline 88 \end{array}$	- - - - - - - - - - - - - -	$\begin{array}{r} 9.2\\ 6.2\\ 1.8\\ 1.9\\ 900\\ 900\\ 1.6\\ 2.1\\ 1.5\\ 1.7\\ 1.8\\ 2.5\\ 2.3\\ 3.0\\ 3.3\\ 4.6\\ 9.4\\ 1900\\ \end{array}$	- - - - - - - - - - - - - -	$\begin{array}{c} 210\\ 900\\ 15\\ 510\\ 5.3\\ 9.8\\ 8.1\\ 65\\ 2.2\\ 5.1\\ 36\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 900\\ 90$	✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓	$\begin{array}{c} 220\\ 7.3\\ 2.0\\ 1.6\\ 19\\ 24\\ 9.8\\ 2.7\\ 1.7\\ 2.0\\ 2.0\\ 2.2\\ 2.5\\ 3.4\\ 3.8\\ 6.3\\ 11\\ 320\\ \end{array}$	 ✓ ✓	$\begin{array}{c} 220\\ 7.3\\ 2.0\\ 1.6\\ 19\\ 24\\ 9.8\\ 2.7\\ 1.7\\ 2.0\\ 2.0\\ 2.2\\ 2.5\\ 3.4\\ 3.8\\ 6.4\\ 11\\ \hline 320\\ \end{array}$	イ イ イ イ イ イ イ イ イ イ イ イ イ イ

Predicate Analysis not Effective

```
int main() {
1
     int p = nondet_int();
2
3
4 if (p) {
       int i;
5
       for (i = 0; i < 1000000; i++);
6
      assert(i >= 1000000);
7
8
 } else {
9
     int x = 5;
10
      int y = 6;
11
       int r = x * y;
12
      assert(r >= x);
13
     }
14
     return 0;
15
16 }
```

Output Condition after Predicate



Program	Explic	it	Predicate		Coml	b. A	Conditional MC	
					Explicit +	Predicate	Explicit + 1	Predicate
token_ring.01.BUG	1.6	-	4.0	\checkmark	5.6	\checkmark	2.6	\checkmark
token_ring.01	1.9	-	8.9	\checkmark	11	\checkmark	2.4	\checkmark
token_ring.02.BUG	2.3	-	24	\checkmark	26	\checkmark	4.3	\checkmark
token_ring.02	1.7	-	900	-	900	-	4.0	\checkmark
token_ring.03.BUG	2.6	-	900	-	900	-	5.4	\checkmark
token_ring.03	2.5	-	900	-	900	-	5.1	\checkmark
token_ring.04.BUG	2.7	-	900	-	900	-	9.1	\checkmark
token_ring.04	2.5	-	900	-	900	-	8.5	\checkmark
token_ring.05.BUG	3.8	-	900	-	900	-	16	\checkmark
token_ring.05	3.2	-	900	-	900	-	17	\checkmark
token_ring.06.BUG	4.8	-	900	-	900	-	34	\checkmark
token_ring.06	5.4	-	900	-	900	-	40	\checkmark
token_ring.07.BUG	9.1	-	900	-	900	-	140	\checkmark
token_ring.07	8.3	-	900	-	900	-	180	\checkmark
token_ring.08.BUG	25	-	900	-	900	-	580	\checkmark
token_ring.08	6.0	-	900	-	900	-	720	\checkmark
token_ring.09.BUG	120	-	900	-	900	-	900	-
token_ring.09	130	-	900	-	900	-	900	-
mem_slave_tlm.1	2.0	-	900	-	900	-	5.2	\checkmark
mem_slave_tlm.2	2.8	-	900	-	900	-	6.3	\checkmark
mem_slave_tlm.3	3.0	-	900	-	900	-	7.5	\checkmark
mem_slave_tlm.4	3.4	-	900	-	900	-	8.1	\checkmark
mem_slave_tlm.5	3.8	-	900	-	900	-	10	\checkmark
toy	2.6	-	900	-	900	-	7.6	\checkmark
Total	350	0	19000	3	19000	3	3600	22
	I		I		I		I	



Sequential Composition





Towards a Unifying Framework

. . .

CPAchecker – History

 2003 – 2008 BLAST (UC Berkeley) Last version 2.5 released in 2008 by B, Jhala, Majumdar, Henzinger

• 2008 – 2012 CPAchecker (SFU, Passau)

- Complete reimplementation of BLAST
 - New, more flexible architecture
 - More efficient algorithms

CPAchecker – Framework

- Software model checker
- Open source (Apache 2), written in Java
- Follows strictly the concept of Configurable Program Analysis [CAV'07, ASE'08]
- Input language: C

CPAchecker – Features

- Integrated most successful SMC ideas:
 - Predicate analysis (1997)
 - CEGAR (2000)
 - Lazy abstraction (2002)
 - Interpolation for predicate discovery (2004)
 - Configurable program analysis (2007)
 - Large block-encoding (2009)
- Strongest domain: Predicate Analysis (faster than BLAST)

Configurable Program Analysis

- Better combination of abstractions
 - Configurable Program Analysis
 [B / Henzinger / Theoduloz CAV'07]



Unified framework that enables intermediate algorithms

Dynamic Precision Adjustment

Better fine tuning of the precision of abstractions
→ Adjustable Precision
[B / Henzinger / Theoduloz ASE'08]

Unified framework enables:

- switch on and off different analyses, and can
- adjust each analysis separately
- Not only refine, also abstract!





Orthogonal Improvement: Block Caching [Wehrheim / Wonisch 2011]

Conditional Verification





Coming Soon

 Competition in Software Verification at TACAS 2013 (March 2013) http://sv-comp.sosy-lab.org

Distributed Model Checking
 → Model Checking in the Cloud

Summary

- Conditional Model Checking
 - \rightarrow Resource-aware (green)
 - → Terminates with Useful Results
 → Effective Sequential Composition
 → Unified View on Existing Approaches
- CPAchecker Verification Framework
 → Designed for Extension & Plug-in
 → One of the Most Efficient
 - Software Model Checkers

Additional Material

Adjustable-Block Encoding

Abstract SuccessorsAbstract state: (Φ, ψ) Φ : Strongest Post ψ : Abstract Formula

Abstract Successors Abstract state: (Φ, ψ) Φ : Strongest Post ψ : Abstract Formula Example: Precision: $\{x > 0\}$ Current abstract state: (true, x > 0) CFA edge: x := 1Successor abstract state: (x = 1, x > 0)After predicate abstraction: (true, x > 0)



Adjustable-Block Encoding

- Boolean Abstraction (not Cartesian)
- Arbitrary Block Size
- We can use more power of SMT SMT Fans: Attention!
- Disjunctions not handled explicitly ART not forced to grow exponentially
- Reduced number of abstractions
- Reduced number of refinements



CPAchecker - Summary

- Unification of several approaches \rightarrow reduced to their essential properties
- Allow experimentation with new configurations that we would never think of
- Flexible implementation as framework

http://cpachecker.sosy-lab.org

```
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Philipp Wendler (Uni Passau)
Daniel Wonisch (Uni Paderborn)
```

CPAchecker Architecture and Flow



CPAchecker - Design



Future: Powerful Parameters ...

	SBE	LBE
SAT check after	error	error
Abstraction after	1	func/loop
Unroll loops	no	no
Inline functions	no	no
Merge	never	non- abstraction

Future: Powerful Parameters ...

	SBE	LBE	BMC
SAT check after	error	error	threshold
Abstraction after	1	func/loop	never
Unroll loops	no	no	yes
Inline functions	no	no	yes
Merge	never	non- abstraction	always

Future: Powerful Parameters ...

	SBE	LBE	McMillan	BMC
SAT check after	error	error	every	threshold
Abstraction after	1	func/loop	never	never
Unroll loops	no	no	yes	yes
Inline functions	no	no	yes	yes
Merge	never	non- abstraction	never	always